PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

			1				
APPL NUM	FILING DATE		SUBCLASS			EXAMINER	- 1
10024892	12/18/2001	438		2812		/] '
**CONTINUING DATA VERIFIED:							
** FOREIGN APPLICATIONS VERIFIED: REPUBLIC OF KOREA 2001-47459 08/07/2001							
PG-PUB DO N	IOT PUBLISH 🖵		RESCIN	ID 🗆			
Foreign priority cla		-	s ⊒ no		ATTO	RNEY DOCKET NO	
35 USC 119 conditions met ☐ yes ☐ no					CILOR	8/1 VE	l l
Verified and Acknowledged Examiners's initials TITLE: Method of fabricating a wafer level package U.S.DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)							

NOTICE OF ALLOWANCE MAILED			CLAIMS ALLOWED				
		Assistant Examiner	Total Claims		Print Claim for O.G		
ISSUE FEE			DRAWING				
Amount Due	Date Paid	7	Sheets Drwg.	Figs.Drwg.	Print Fig.		
		Primary Examiner					
TERMINAL		PREPARED FOR ISSUE Application Examiner					
	DISCLAMER	WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.					
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